



Please amend the fourth full paragraph on page 21 as follows:

Fig. 15B is a block diagram showing the construction of the horizontal line processing circuit in detail;

Please amend the second full paragraph on page 36 as follows:

Fig. 15B shows an example of the construction of the horizontal line processing circuit 2. In this example, the Y coordinate value Y_N provides as it is a pixel Y coordinate Y_P . Upon the start of processing of one scan line, the X coordinate X_P of the initially processed pixel on the scan line is stored in an X_P register 111. This X coordinate X_P is selected by a selector 112, and its initial value is X_{Le} . Also, X_{Le} , X_{Li} , X_{Ri} , X_{Re} are respectively set in corresponding registers 113, 114, 115 and 116. An adder 110 sequentially adds "1" to the X coordinate X_P so that the X coordinate X_P is shifted to the right by one at a time along the scan line. Comparators 117, 118, 119 and 129 compare this X coordinate X_P with X_{Le} , X_{Li} , X_{Ri} , and X_{Re} , respectively, and a controller 121 controls respective portions of the circuit 2 based on the results of comparison.